

FIG. 1

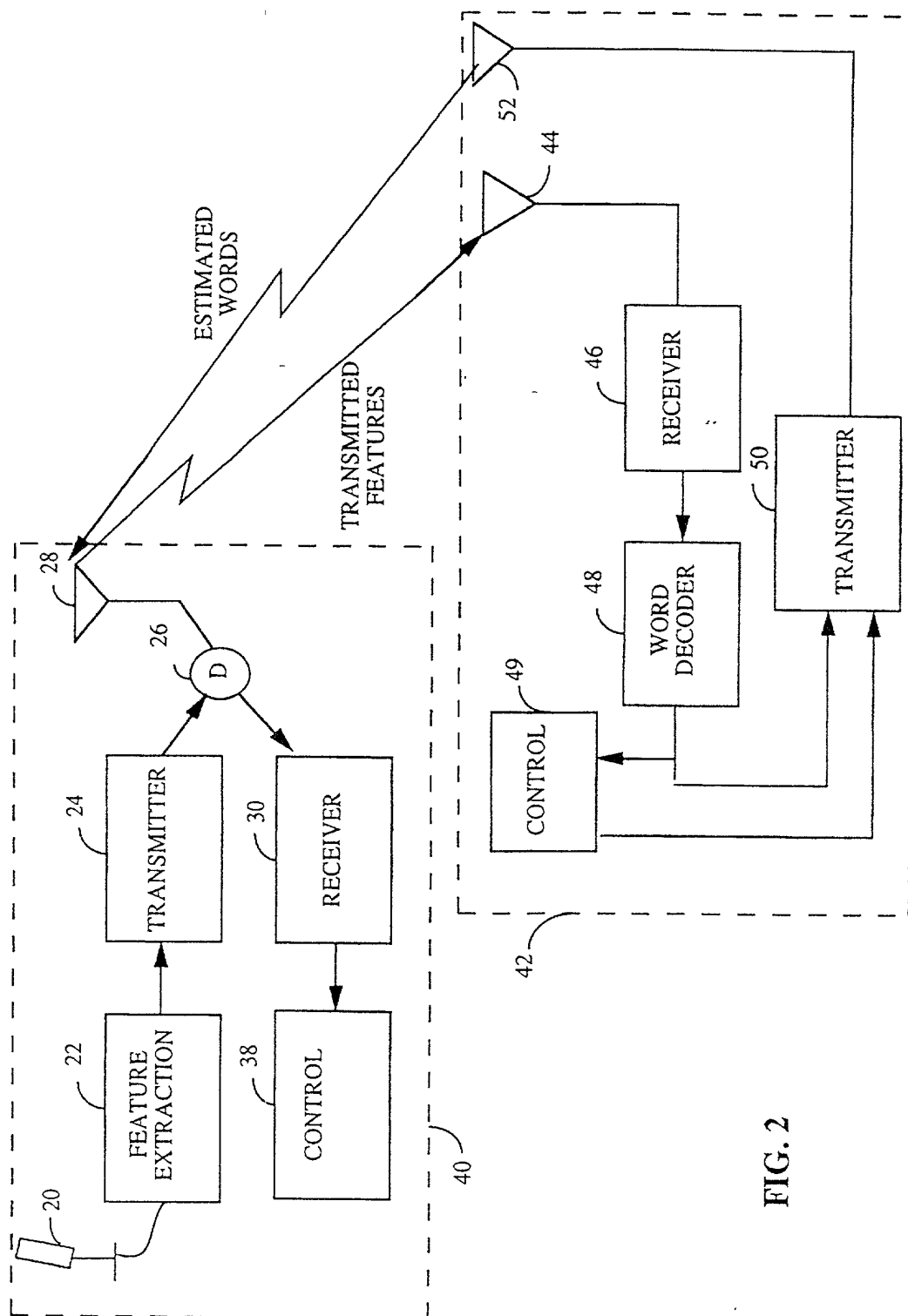


FIG. 2

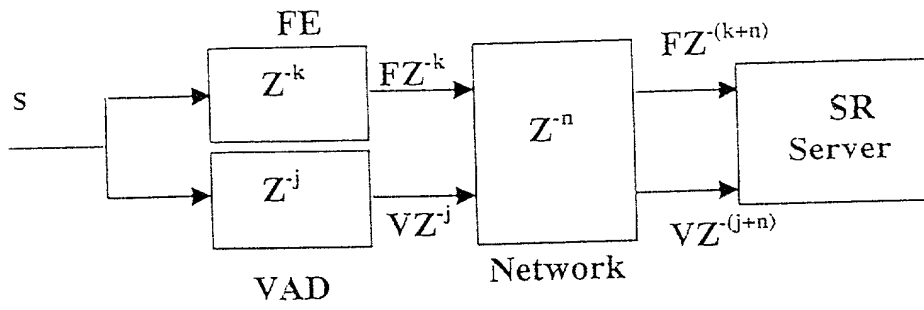


FIG. 3

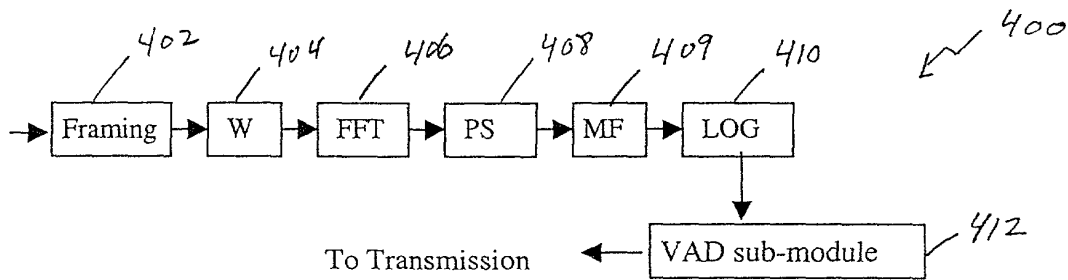


Figure 4. Block diagram of the VAD

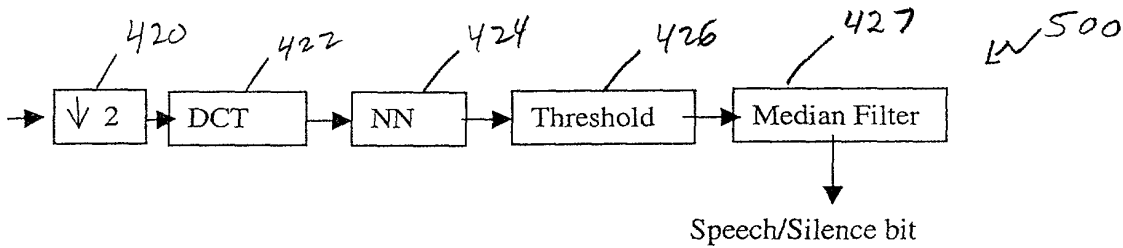


Figure 5. Block diagram of Voice Activity Detector sub-module

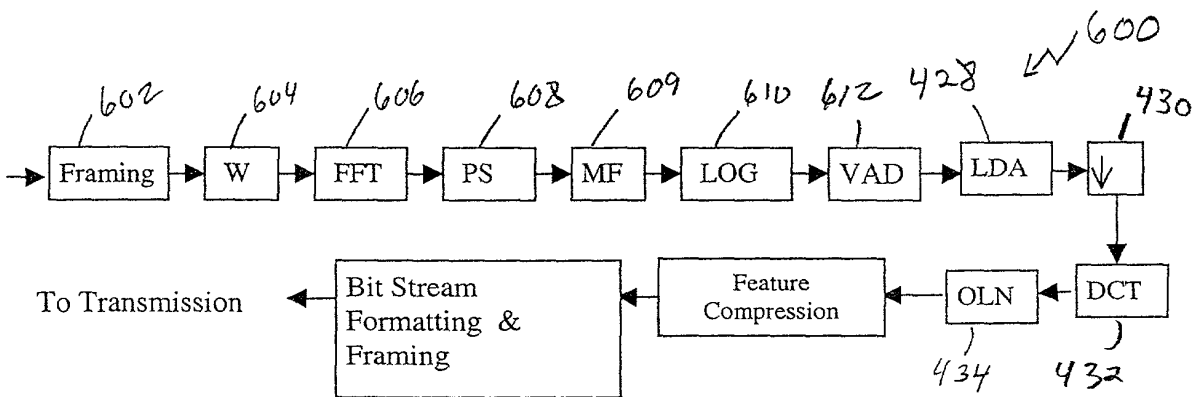
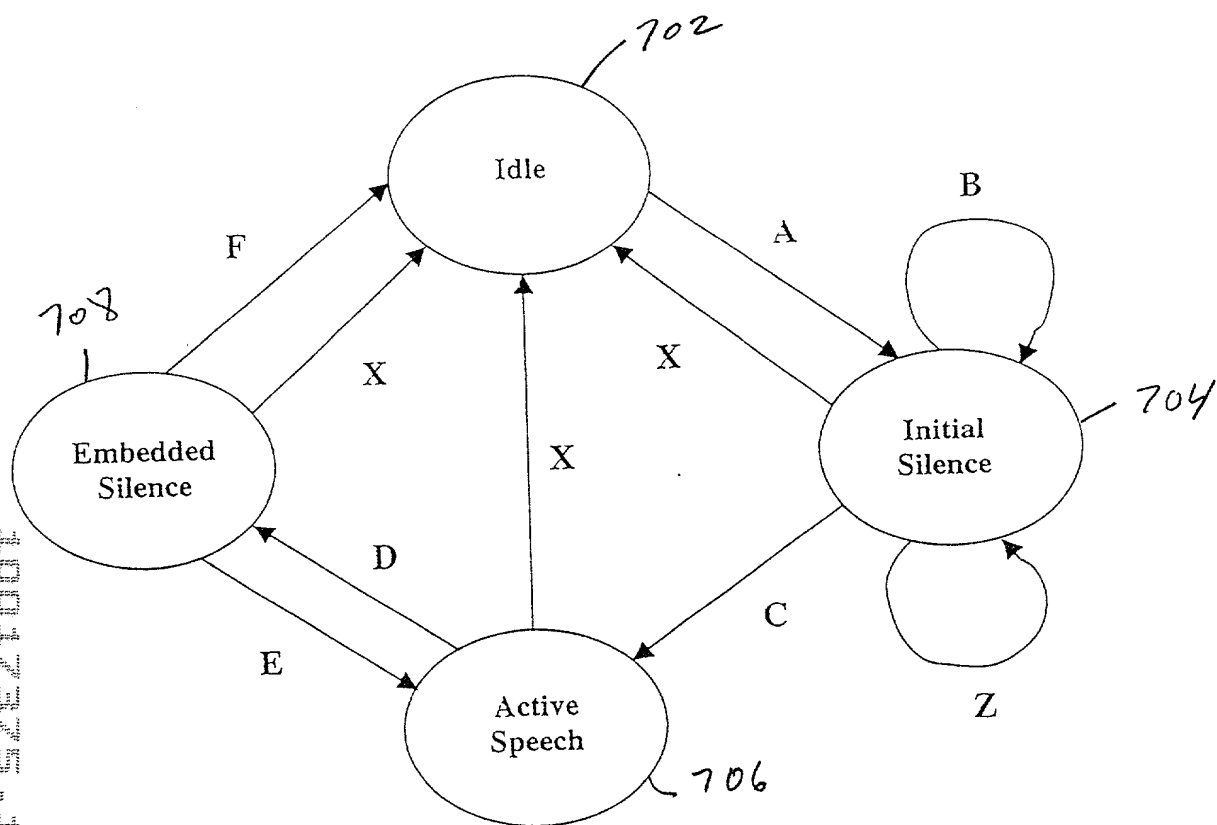


Figure 6. Block diagram of the FE module
(Terminal Feature Extraction and Terminal Compression)

FIG. 7



Energy

VAD

FE Payload

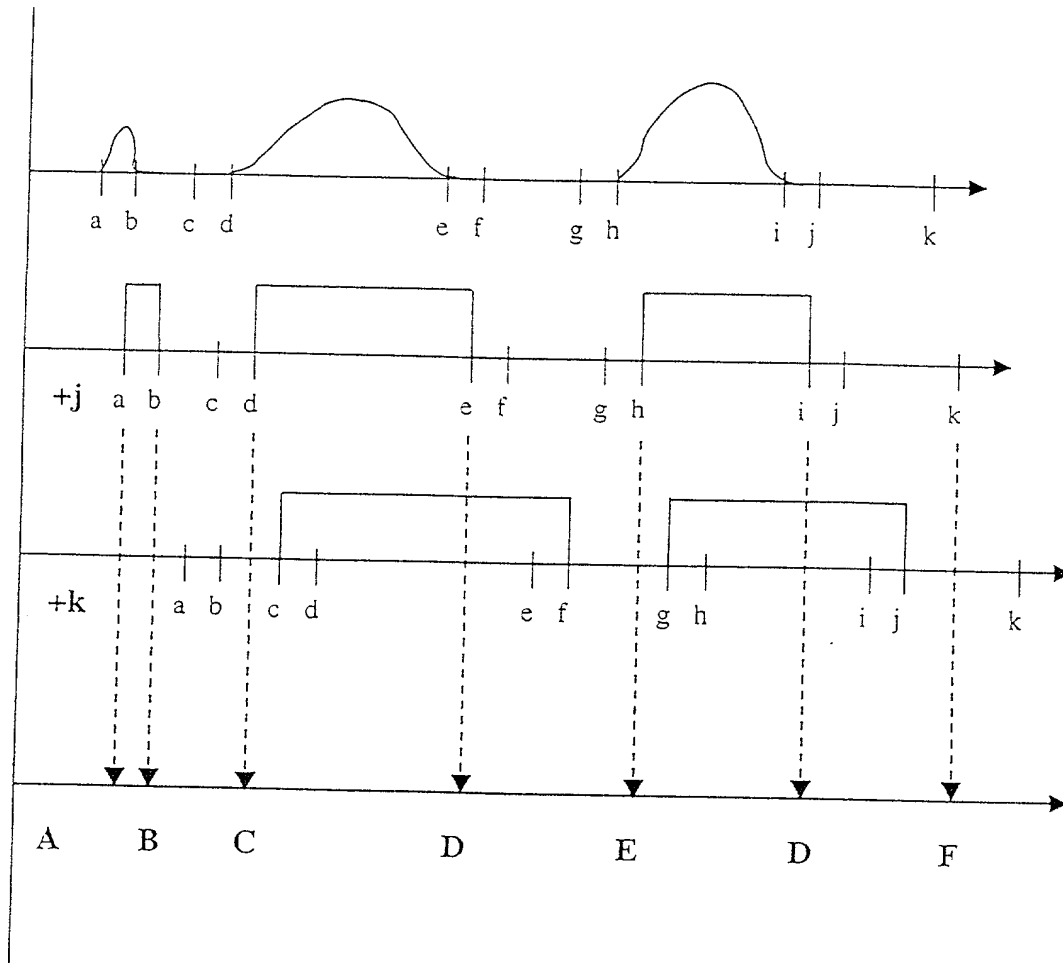


FIG. 8